



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi KATAKURA, et al.**

Serial No.: **09/522,470**

Filed: **March 9, 2000**

For: **LOGIC CIRCUIT**

Group Art Unit: **2124**

Examiner: **Chat C. Do**

P.T.O. Confirmation No.: 3147

RESPONSE TO THE RESTRICTION REQUIREMENT
DATED November 20, 2002

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Technology Center 2100

Commissioner for Patents
Washington, D.C. 20231

Date: December 16, 2002

Sir:

This paper is submitted in response to the Official Action dated **November 20, 2002**.

In the Action, restriction is required among **Group (I)**, Claims 1-2 and 7-8, drawn to a leaf cell; **Group (II)**, Claims 3-5 and 10-11, drawn to a logic circuit for magnitude comparison; **Group (III)**, Claims 6 and 12, drawn to a logic circuit for adding signals; and **Group (IV)**, claim 9, drawn to a logic circuit for adding signals with carry generation.

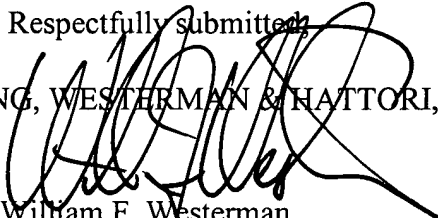
Applicants hereby elect the subject matter of **Group (I)**, Claims 1-2 and 7-8, drawn to a leaf cell, for prosecution in this application. This election is made without traverse, it being understood that the applicants' rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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